

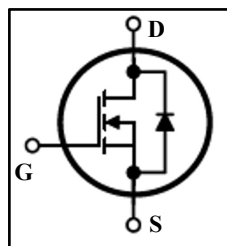
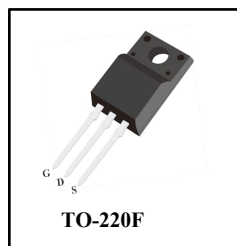


### FEATURES

- $BV_{DSS}$ : 650V,  $I_D=12A$
- $R_{DS(on)}$  : 0.8Ω(Max) @ $V_{GS}=10V$
- Very Low FOM ( $R_{DS(on)} * Q_g$ )
- Excellent stability and uniformity

### APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- AC to DC Converters



Ordering Information		
Type NO.	Marking	Package Code
MPVA12N65F	MPVA12N65F	TO-220F

Absolute Maximum Ratings $T_C = 25^\circ C$ , unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage ( $V_{GS} = 0V$ )	$V_{DSS}$	650	V
Continuous Drain Current	$I_D$	12	A
Pulsed Drain Current (note1)	$I_{DM}$	48	A
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Single Pulse Avalanche Energy (note2)	$E_{AS}$	900	mJ
Avalanche Current (note1)	$I_{AR}$	9	A
Repetitive Avalanche Energy (note1)	$E_{AR}$	54	mJ
Power Dissipation ( $T_C = 25^\circ C$ )	$P_D$	70	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	$^\circ C$

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.78	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	



芯基科技

# MPVA12N65F Power MOSFET

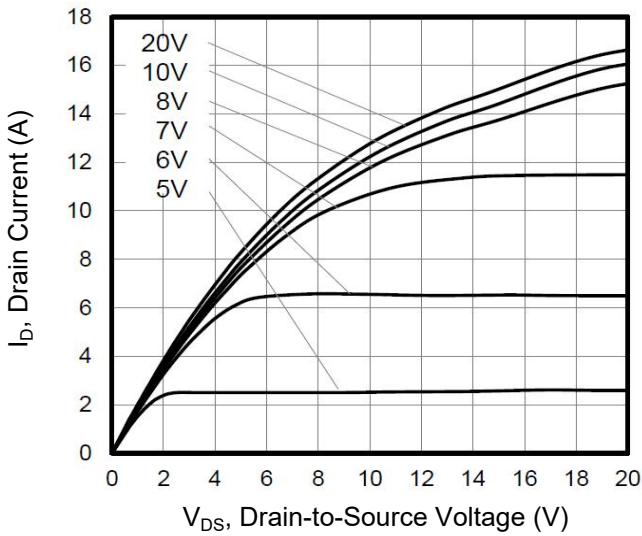
Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 30V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0	--	4.0	V
Drain-Source On-Resistance (Note4)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 6A$	--	0.62	0.80	$\Omega$
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 25V,$ $f = 1.0\text{MHz}$	--	1943	--	pF
Output Capacitance	$C_{oss}$		--	132	--	
Reverse Transfer Capacitance	$C_{rss}$		--	13	--	
Total Gate Charge	$Q_g$	$V_{DD} = 520V, I_D = 12A,$ $V_{GS} = 10V$	--	36	--	nC
Gate-Source Charge	$Q_{gs}$		--	9	--	
Gate-Drain Charge	$Q_{gd}$		--	22	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 325V, I_D = 12A,$ $R_G = 25\Omega$	--	58	--	ns
Turn-on Rise Time	$t_r$		--	20	--	
Turn-off Delay Time	$t_{d(off)}$		--	104	--	
Turn-off Fall Time	$t_f$		--	50	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	12	A
Pulsed Diode Forward Current	$I_{SM}$		--	--	48	
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 12.0A, V_{GS} = 0V$	--	--	1.4	V
Reverse Recovery Time	$t_{rr}$	$V_R = 400V, I_F = 12.0A,$ $di_F/dt = 100A/\mu\text{s}$	--	380	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	4.5	--	$\mu\text{C}$

### Notes

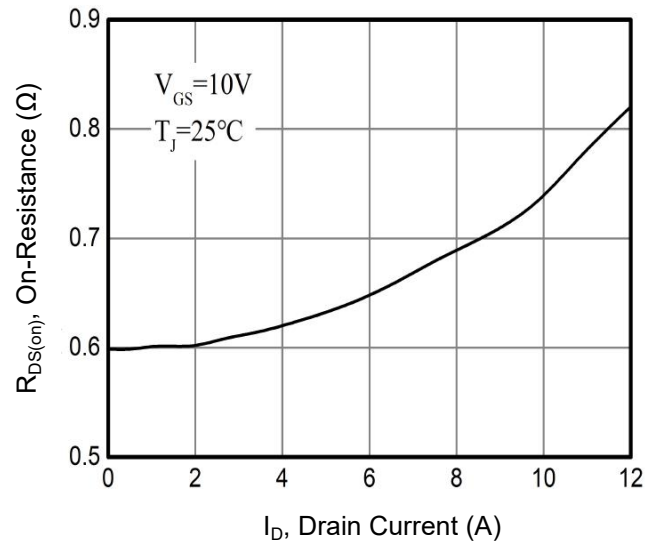
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 9A, V_{DD} = 50V, R_G = 25\Omega, \text{Starting } T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$
4. Essentially independent of operating temperature

Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

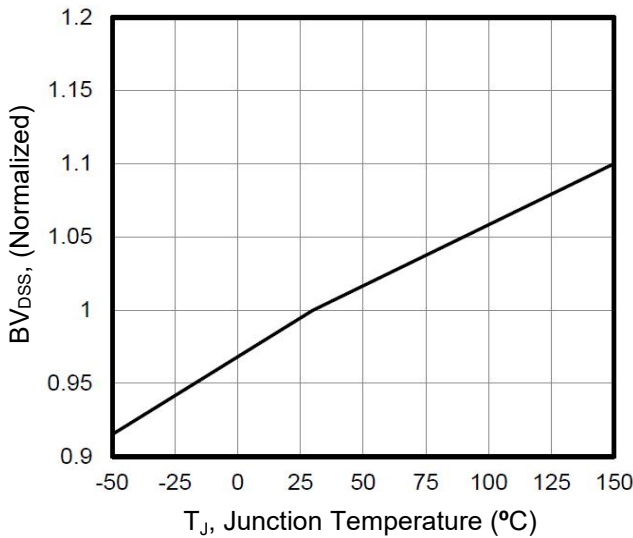
**Figure 1. Output Characteristics**



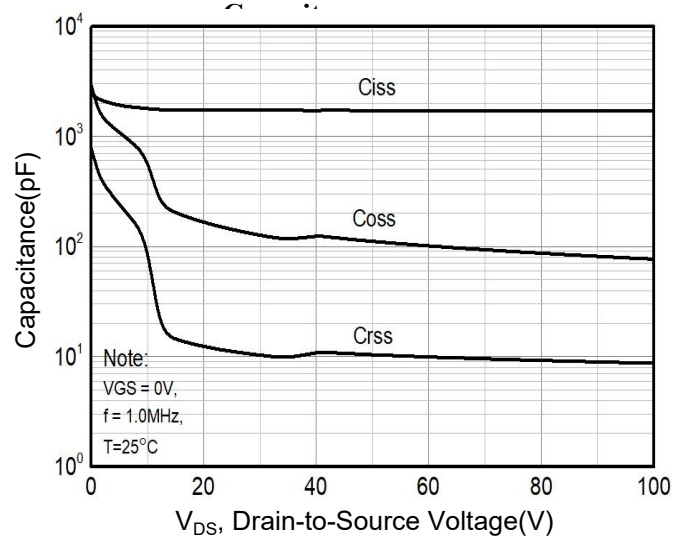
**Figure 2. On-Resistance vs. Drain Current**



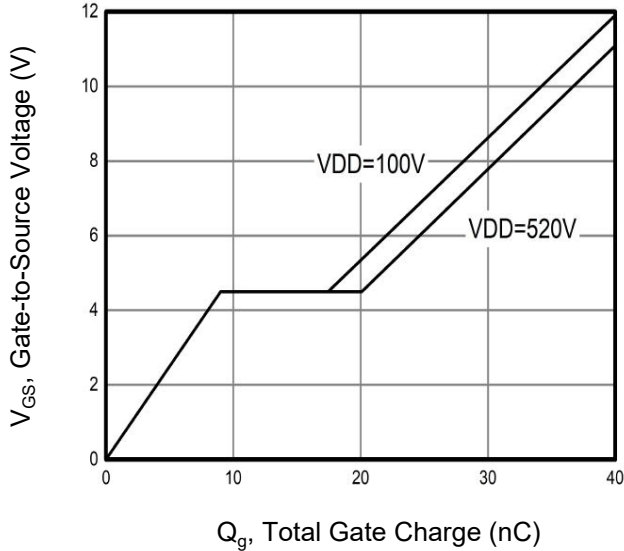
**Figure 3.  $BV_{DSS}$  vs. Temperature**



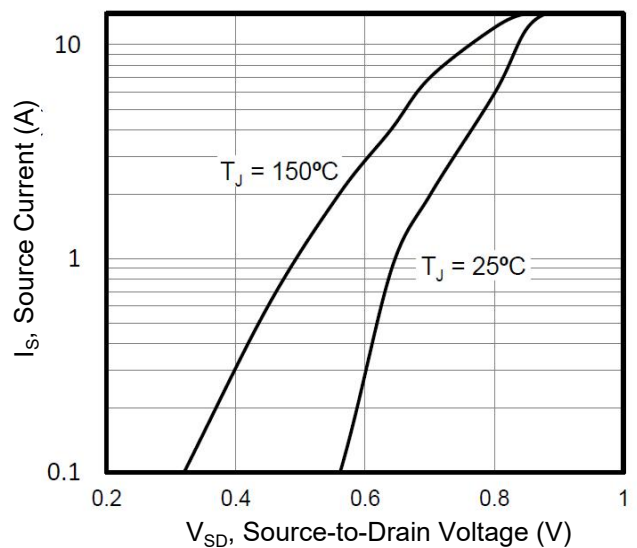
**Figure 4.**



**Figure 5. Gate Charge**

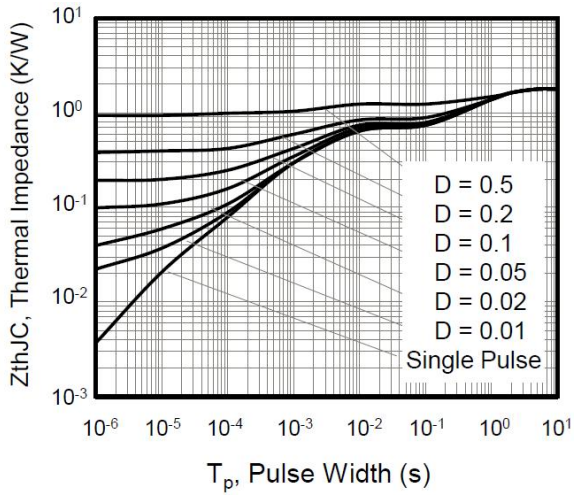


**Figure 6. Body Diode Forward Voltage**

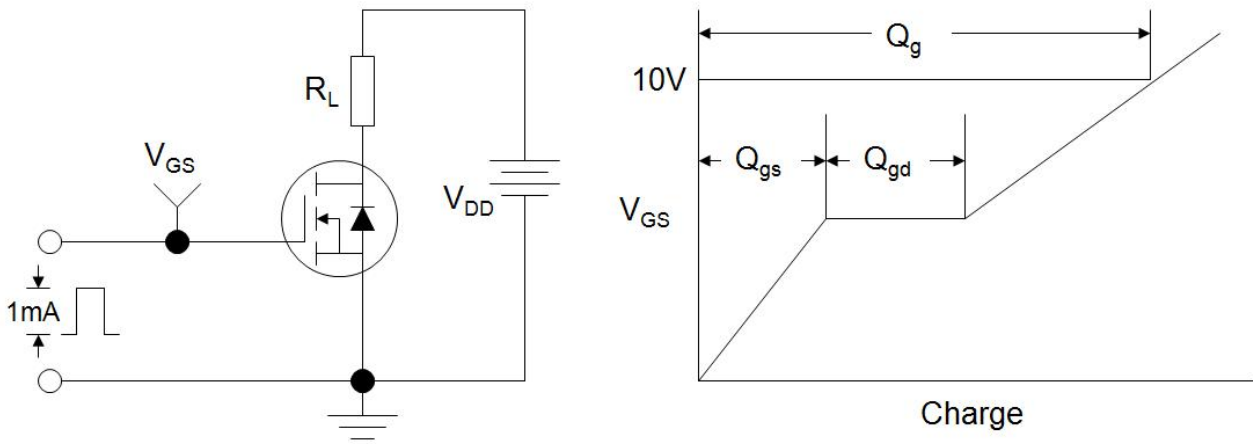


Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

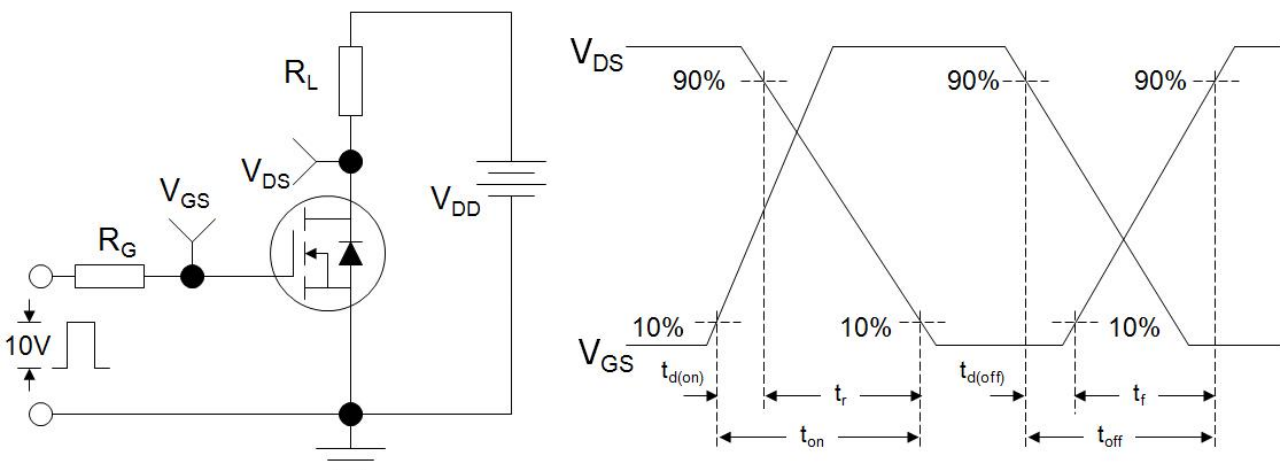
Figure 7. Transient Thermal Impedance



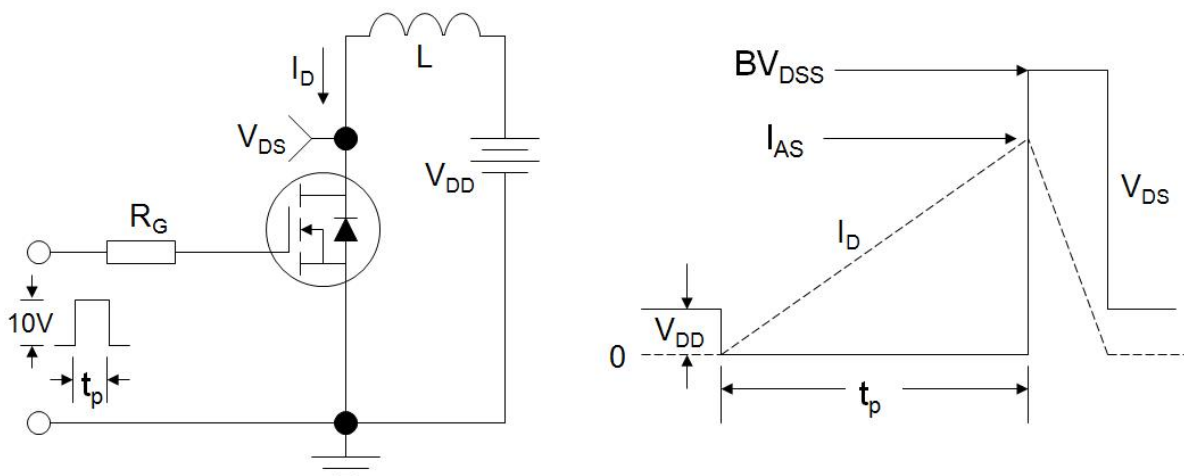
**Figure A: Gate Charge Test Circuit and Waveform**



**Figure B: Resistive Switching Test Circuit and Waveform**



**Figure C: Unclamped Inductive Switching Test Circuit and Waveform**



**Outline Dimension**

Unit: mm

**TO-220F**

